

### **National Journal Publications (2013-2014):**

1. N. Hanumantha Rao, N. Rajasekhar Babu, Y. Madhulika, "Analysis of Cache- Coherence protocols for Multi-Core Architectures" Journal of Innovation in Electronics and Communication, Vol-3,issue-1,pp.31-36,Jan-June 2013,ISSN NO:2249-9946.